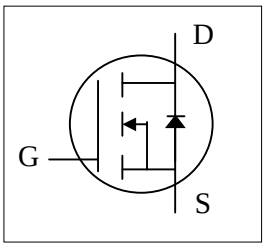
AP9977GH/J-HF

Halogen-Free Product

Advanced Power Electronics Corp.

N-CHANNEL ENHANCEMENT MODE POWER MOSFET

- ▼ **Low Gate Charge**
- ▼ **Single Drive Requirement**
- ▼ **Surface Mount Package**
- ▼ **RoHS Compliant**

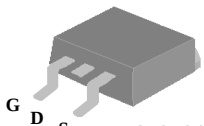


BV_{DSS}	60V
$R_{DS(ON)}$	100mΩ
I_D	11A

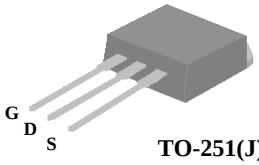
Description

Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-252 package is widely preferred for all commercial-industrial surface mount applications and suited for low voltage applications such as DC/DC converters. The through-hole version (AP9977GJ) are available for low-profile applications.



TO-252(H)



TO-251(J)

Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	60	V
V_{GS}	Gate-Source Voltage	±25	V
I_D@T_C=25℃	Continuous Drain Current, V_{GS} @ 10V	11	A
I_D@T_C=100℃	Continuous Drain Current, V_{GS} @ 10V	6.8	A
I_{DM}	Pulsed Drain Current[1]	45	A
P_D@T_C=25℃	Total Power Dissipation	21	W
	Linear Derating Factor	0.17	W/℃
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	6	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient (PCB mount)[3]	62.5	℃/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	110	℃/W

Electrical Characteristics@T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V, I_D=250uA	60	-	-	V
$\Delta BV_{DSS}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to 25℃, I_D=1mA	-	0.04	-	V/℃
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=10V, I_D=5A	-	-	100	mΩ
		V_{GS}=4.5V, I_D=4A	-	-	125	mΩ
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS}=V_{GS}, I_D=250uA	1	-	3	V
g_{fs}	Forward Transconductance	V_{DS}=10V, I_D=5A	-	7	-	S
I_{DSS}	Drain-Source Leakage Current	V_{DS}=60V, V_{GS}=0V	-	-	10	uA
	Drain-Source Leakage Current (T_j=125°C)	V_{DS}=48V ,V_{GS}=0V	-	-	250	uA
I_{GSS}	Gate-Source Leakage	V_{GS}=±25V, V_{DS}=0V	-	-	±100	nA
Q_g	Total Gate Charge[2]	I_D=5A	-	6	10	nC
Q_{gs}	Gate-Source Charge	V_{DS}=48V	-	2	-	nC
Q_{gd}	Gate-Drain ("Miller") Charge	V_{GS}=4.5V	-	3	-	nC
$t_{d(on)}$	Turn-on Delay Time[2]	V_{DS}=30V	-	6	-	ns
t_r	Rise Time	I_D=5A	-	11	-	ns
$t_{d(off)}$	Turn-off Delay Time	R_G=3.3Ω,V_{GS}=10V	-	14	-	ns
t_f	Fall Time	R_D=6Ω	-	2	-	ns
C_{iss}	Input Capacitance	V_{GS}=0V	-	485	780	pF
C_{oss}	Output Capacitance	V_{DS}=25V	-	55	-	pF
C_{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	40	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V_{SD}	Forward On Voltage[2]	I_S=5A, V_{GS}=0V	-	-	1.2	V
t_{rr}	Reverse Recovery Time[2]	I_S=5A, V_{GS}=0V,	-	23	-	ns
Q_{rr}	Reverse Recovery Charge	dI/dt=100A/μs	-	28	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

3.Surface mounted on 1 in^2 copper pad of FR4 board

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

APEC RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN.

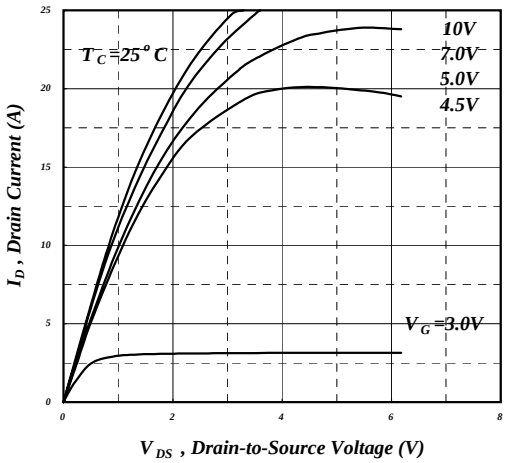


Fig 1. Typical Output Characteristics

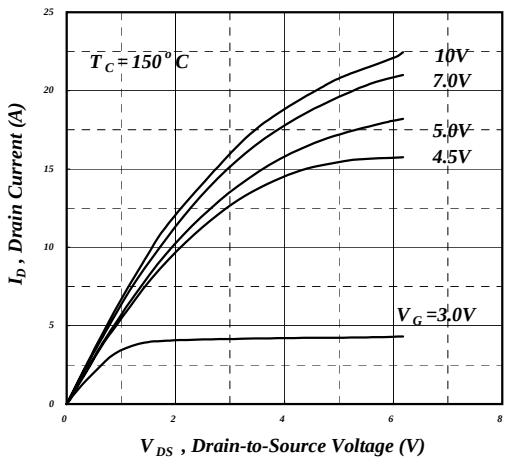


Fig 2. Typical Output Characteristics

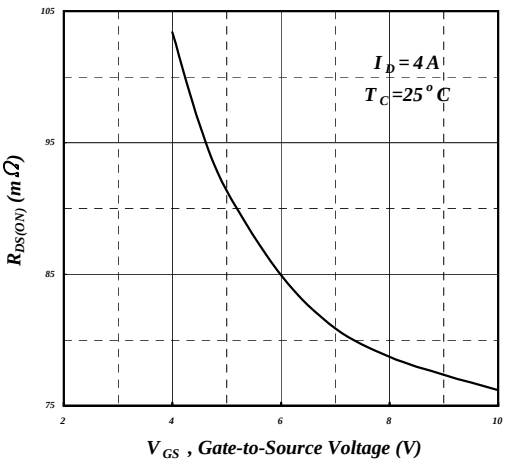


Fig 3. On-Resistance v.s. Gate Voltage

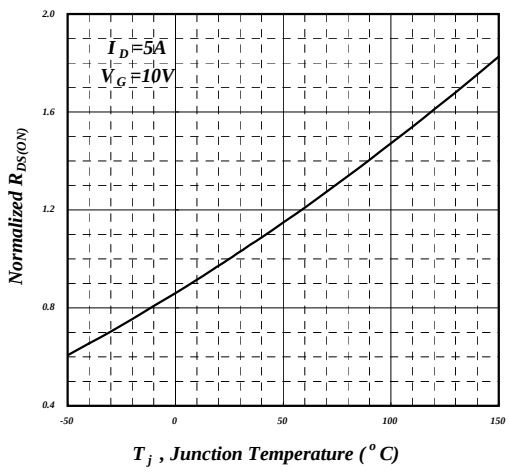


Fig 4. Normalized On-Resistance v.s. Junction Temperature

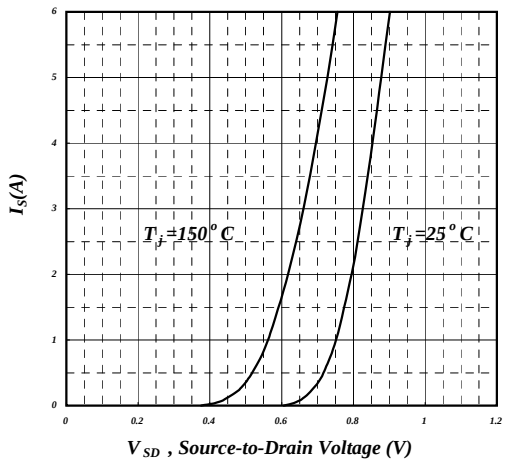


Fig 5. Forward Characteristic of Reverse Diode

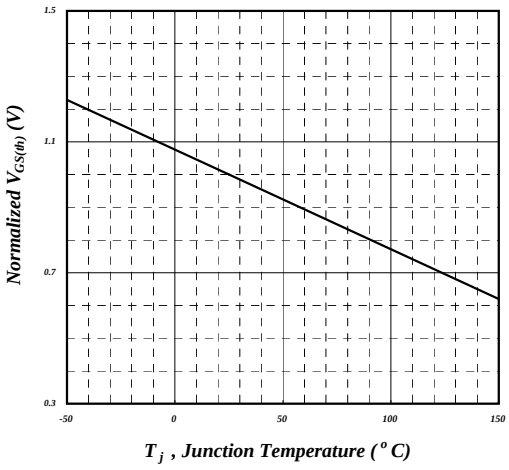


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

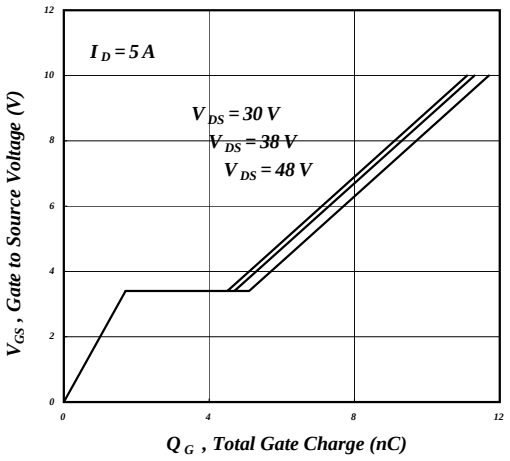


Fig 7. Gate Charge Characteristics

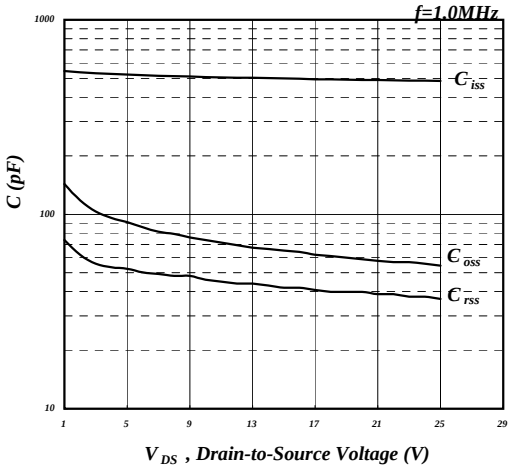


Fig 8. Typical Capacitance Characteristics

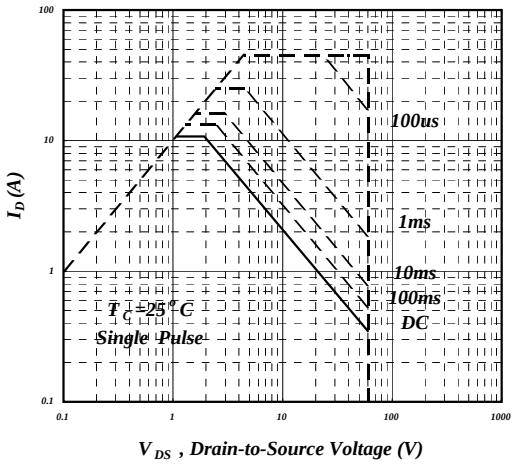


Fig 9. Maximum Safe Operating Area

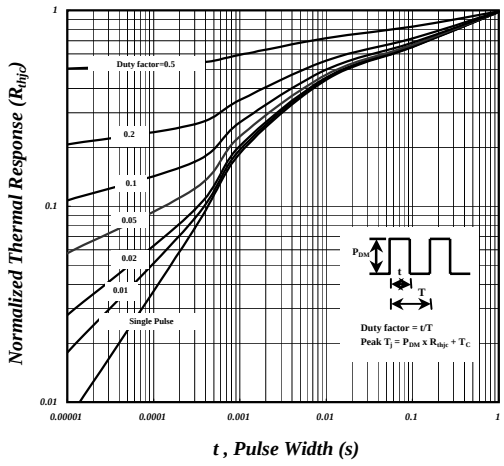


Fig 10. Effective Transient Thermal Impedance

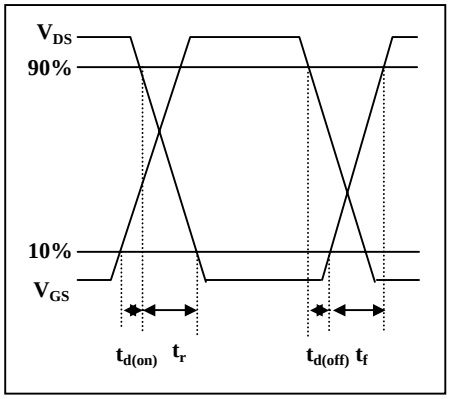


Fig 11. Switching Time Waveform

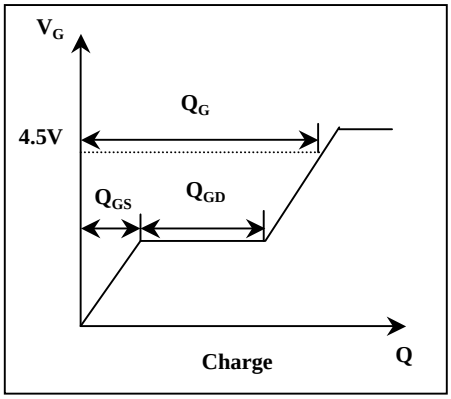


Fig 12. Gate Charge Waveform